

### April 20-22, 2008 Austin, Texas

GENERAL CHAIR Sandhya Dwarkadas, University of Rochester (sandhya@cs.rochester.edu)

> PROGRAM CHAIR Dean Tullsen, UC San Diego (tullsen@cs.ucsd.edu)

### **PROGRAM COMMITTEE**

Michael Adler, Intel David Brooks, Harvard University David Christie, AMD Jamison Collins, Intel Lieven Eeckhout, Ghent University Babak Falsafi, CMU Paolo Faraboschi, HP Annie Foong, Intel Mike Gschwind, IBM Rajiv Gupta, UC Riverside Sudhanva Gurumurthi, University of Virginia Ravi Iyer, Intel Rakesh Kumar, UIUC Mikko Lipasti, Univ. of Wisconsin Gabriel Loh, Georgia Tech Geoff Lowney, Intel Vijay Pai, Purdue University Scott Rixner, Rice University Yiannakis Sazeides, University of Cyprus Tim Sherwood, UCSB Jim Smith, University of Wisconsin Mark Squillante, IBM Pen-Chung Yew, University of Minnesota

LOCAL ARRANGEMENTS CHAIR Nasr Ullah, Freescale

**PUBLICITY CHAIR** Elmoustapha Ould-Ahmed-Vall, Intel

> **FINANCE CHAIR** Nadeem Malik, IBM

WEB CHAIR Byeong Kil Lee, Texas Instruments

SUBMISSION AND REVIEW WEB CHAIR Leo Porter, UCSD

> **PUBLICATIONS CHAIR** Russ Joseph, Northwestern University

WORKSHOPS/TUTORIALS CHAIR Tao Li, University of Florida

**REGISTRATION CHAIR** Rajeev Balasubramonian, University of Utah

# **CALL FOR PAPERS**

## International Symposium on Performance Analysis of Systems and Software – ISPASS-2008



Sponsored by the IEEE Computer Society's TCI, TCCA, and TC-uARCH



The IEEE International Symposium on Performance Analysis of Systems and Software provides a forum for sharing advanced academic and industrial research work focused on performance analysis in the design of computer systems and software. Authors are invited to submit previously unpublished work for possible presentation at this conference. Papers are solicited in fields that include the following:

- Benchmarking
- Workload characterization
- Simulation
- Analytical models
- Statistical approaches
- Performance metrics
- Microprocessor, memory, and disk performance issues
- Multi-core, multithreaded, and multiprocessor performance issues
- Performance of computer networks
- Performance analysis of software
- Performance of data-intensive applications
- Tuning of application code
- Tuning of system code
- Tracing, profiling, and simulation tools
- Bottleneck identification
- Power and thermal modeling
- Performance validation
- Characterization of emerging applications
- Case studies
- Confirmations or refutations of important prior results

Papers of no more than 22 double-spaced pages (no less than 11pt font), including figures, are solicited. Authors are requested to submit papers in PDF format. More information will be available on the ISPASS website (http://ispass.org) as the submission deadline approaches.

### **Important Dates**

Abstract due: September 21, 2007 Full submission due: September 28, 2007 (no extensions) Notification of acceptance: December 17, 2007 Final version due: February 22, 2008

For more information, visit the ISPASS web site at http://ispass.org